

MULTIPLE MEMORY SYSTEM SUPPORT THROUGH SEGMENT ASSIGNMENT

ABSTRACT

- 5 A method of increasing computer system bandwidth for computer system having two or more memory complexes is disclosed in which exclusive OR operations are performed on the data from the data regions to generate parity information which is stored in the same single cache pool as the data regions. By using a single cache pool for related data regions, bandwidth and performance are improved.

10

1004203-10101